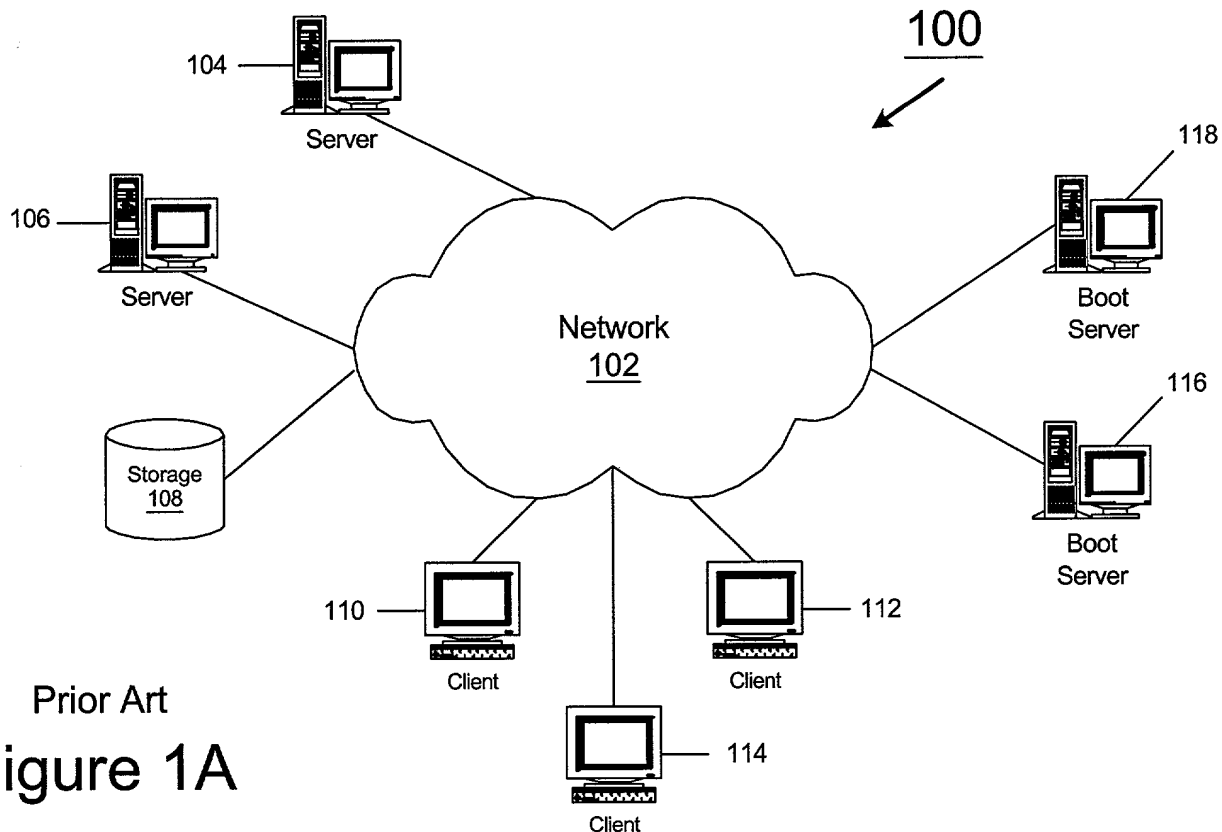
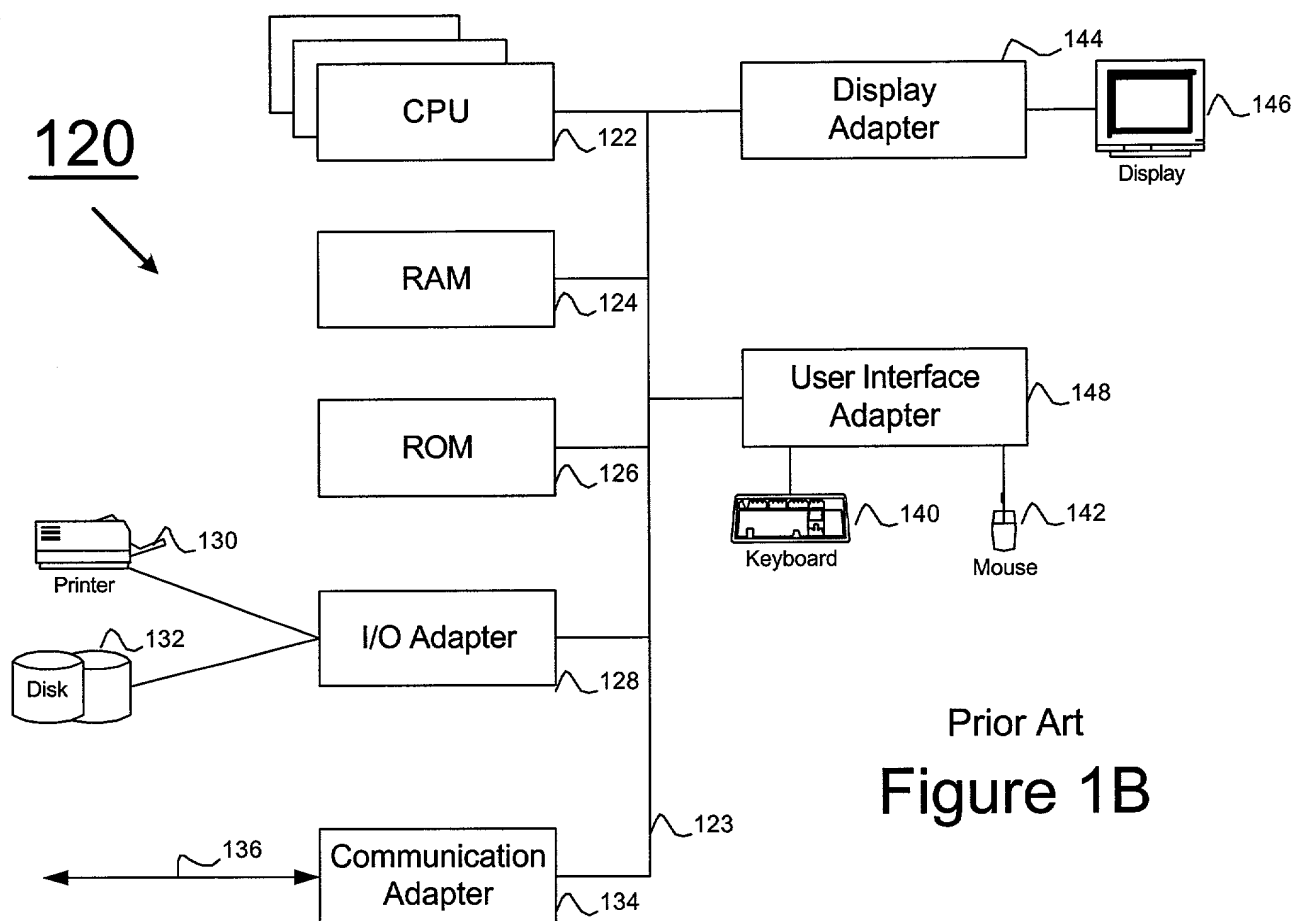


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AUS920000858US1

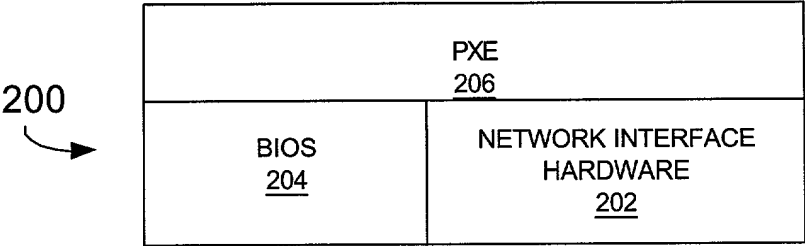


Prior Art
Figure 1A



Prior Art
Figure 1B

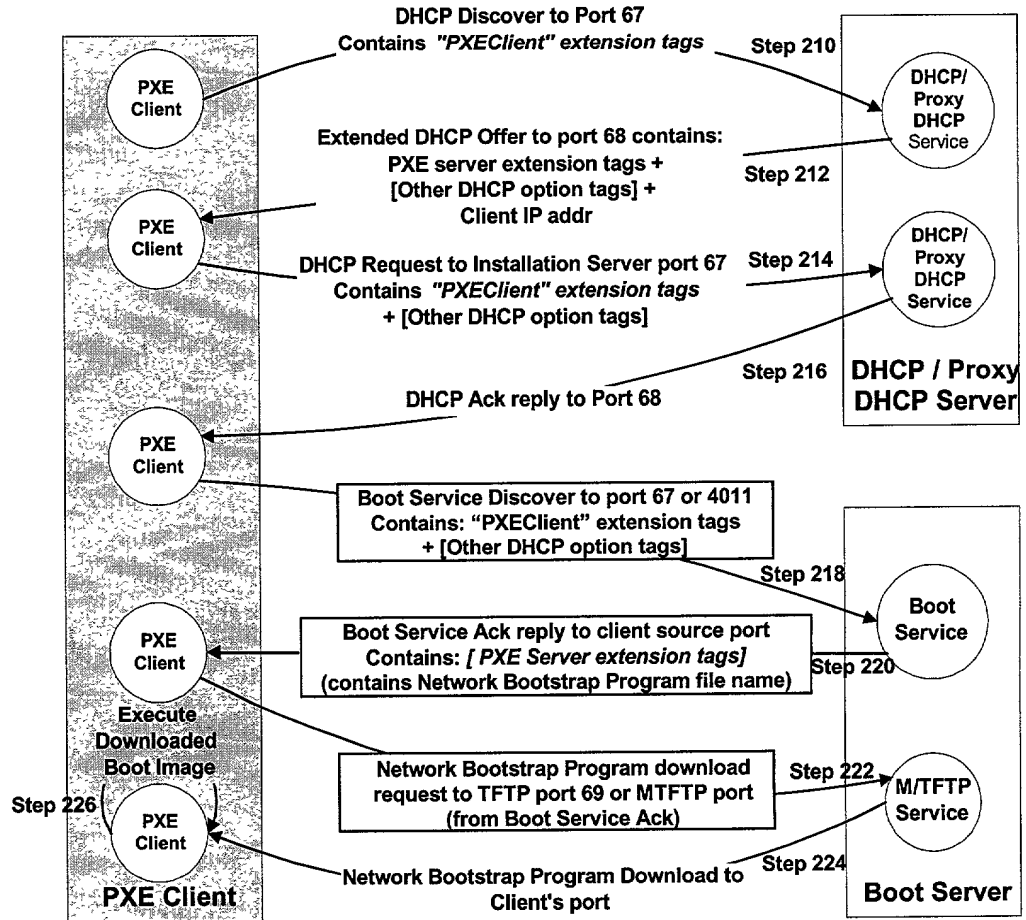
FIG. 1A and FIG. 1B



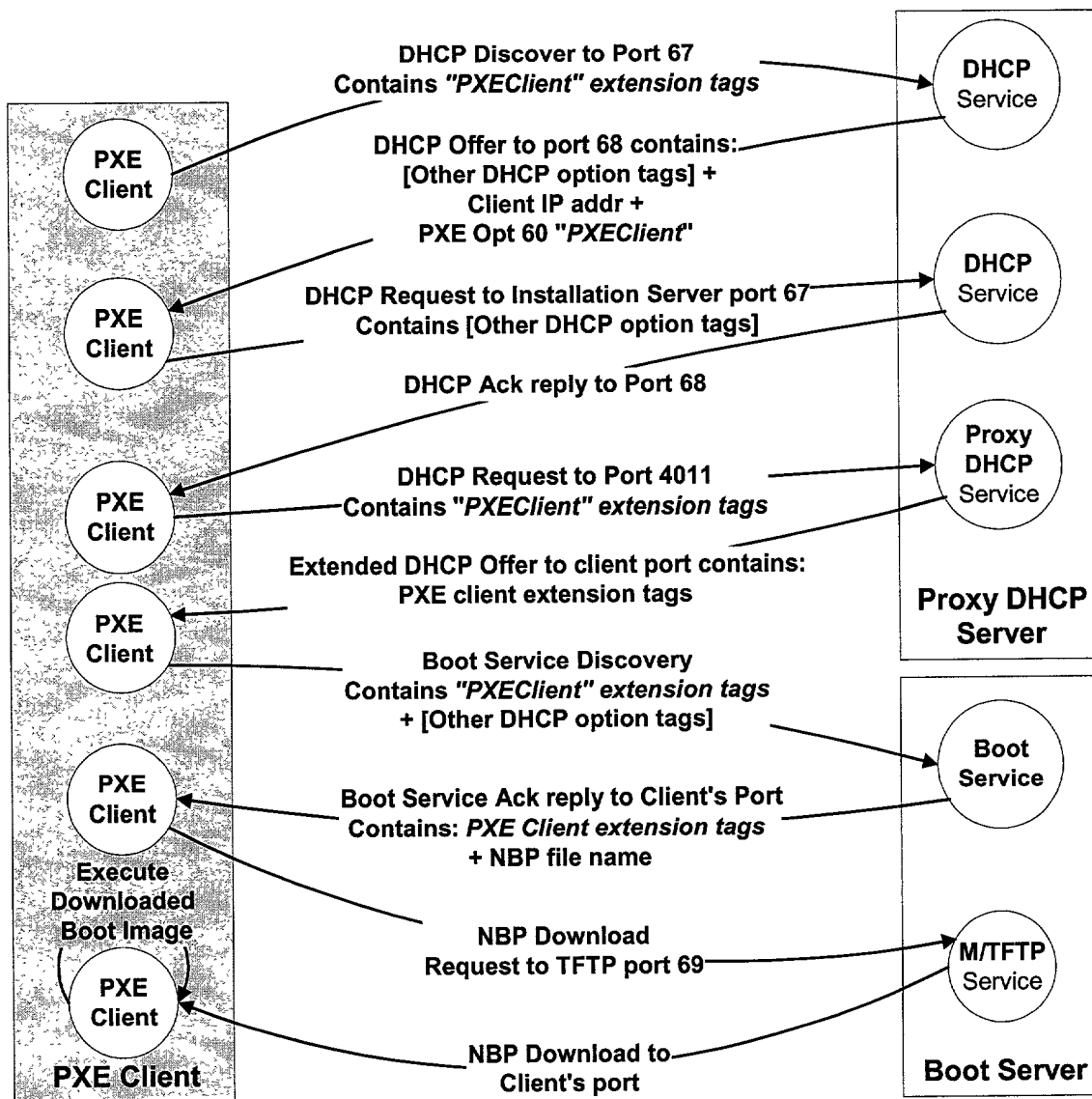
Prior Art

Figure 2A

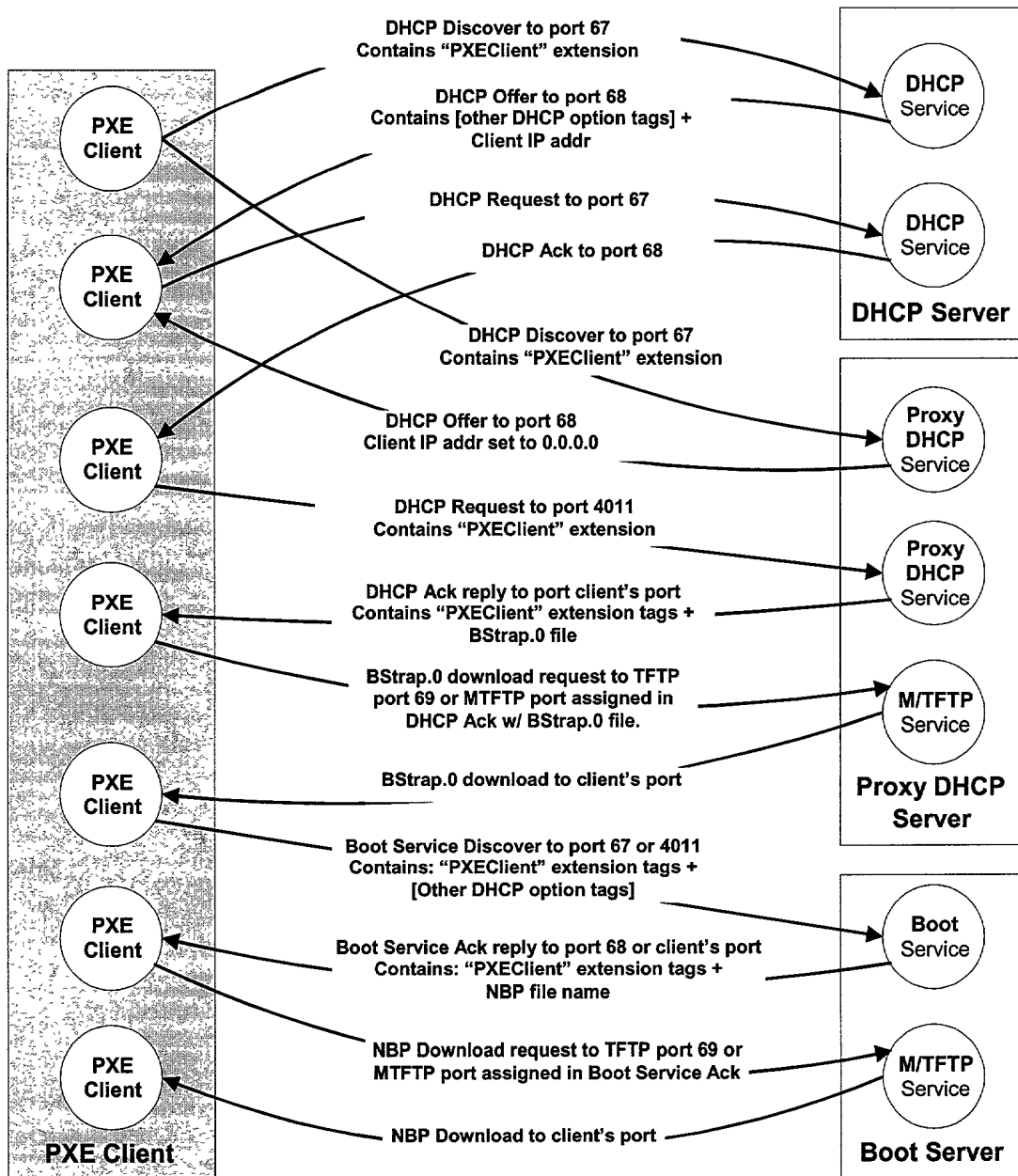
FIG. 2A is a block diagram of a system architecture 200.



Prior Art
Figure 2B



Prior Art
Figure 2C



Prior Art
Figure 2D

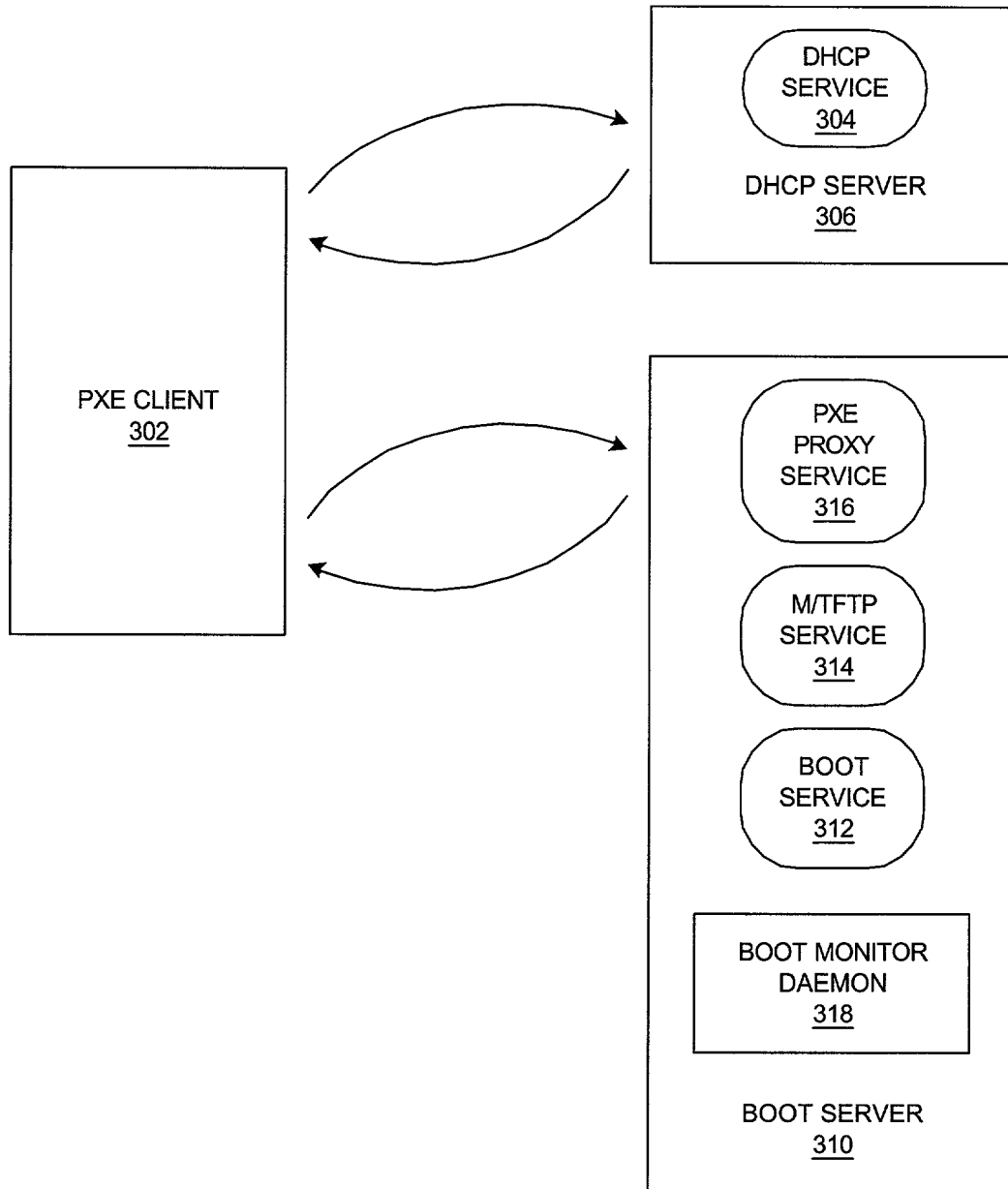


Figure 3

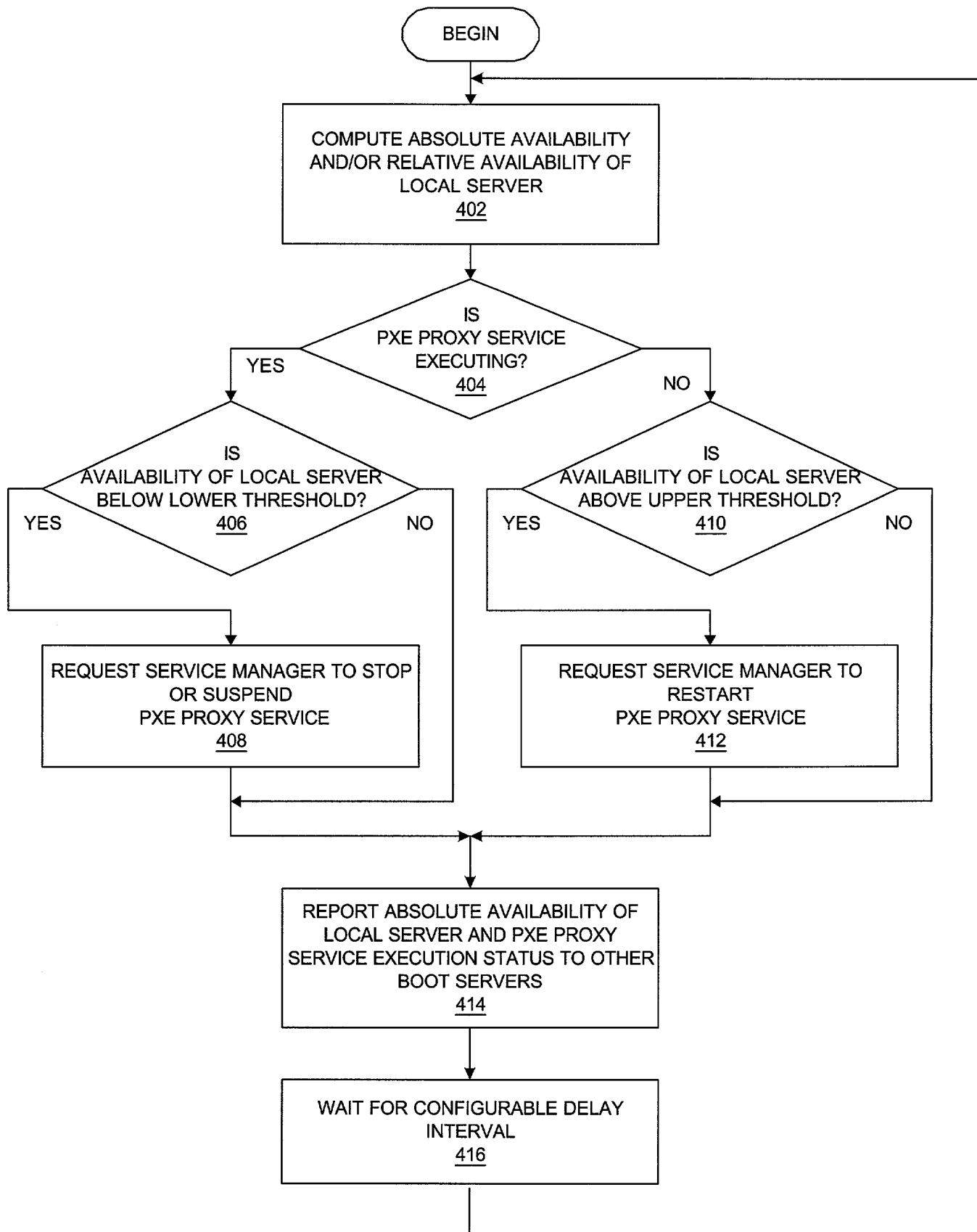


Figure 4